

USB4 Product Test Matrix

		Host		Hub/Dock		Peripheral Device	
		Silicon	End Product	Silicon	End Product	Silicon	End Product
USB4 Compliance Test Suites	Electrical	✓	✓	✓	✓	✓	✓
	Logical Layer	✓	Note 1	✓	Note 1	✓	Note 1
	Protocol	✓	✗	✓	✗	✓	✗
	TMU	✓	✗	✓	✗	✓	✗
	H2H Tunneling	✓	✗	✗	✗	✗	✗
	USB3 Tunneling	✓	✗	✓	✗	Note 2	✗
	DP Tunneling	✓	✗	✓	✗	Note 2	✗
	PCIe Tunneling	Note 2	✗	✓	✗	Note 2	✗
	USB4 Interop	✓	✓	✓	✓	✓	✓
	TBT3-Compatibility	Note 2	✗	Note 2	✗	Note 2	✗

Note 1. Logical Layer testing is only required if re-timers have been added, removed, or changed after silicon certification.

Note 2. Only tested if feature is supported.